

專題演講

Pushing the Frontiers of Optimization Performance

演講者: Dr. Robert Bixby

- 美國國家工程學院院士
- GUROBI 總裁及聯合創始人
- CPLEX 創辦人
- Noah Harding Professor Emeritus of Computational and Applied Mathematics at Rice University, USA

時間: 9/17 (週三) · 09:45 ~11:45am

地點: 國立臺北科技大學綜合科館第二演講廳 (由忠孝東路校門口進入後右手邊大樓地下室)

交通: 台北市忠孝東路三段一號 (台北捷運板南線或中和新蘆線忠孝新生站四號出口)

活動簡述:

美國國家工程學院(NAE)院士·CPLEX 創辦人 Dr. Robert Bixby 將於 9 月中來台。由於其在 Combinatorial Optimization 高性能最佳化軟體上的研發及商用化的卓越貢獻·Dr.Bixby 於 1997 年便被選為 NAE 院士·是作業研究在實際商用計算運用的世界級大師。此次於台北科大演講·Dr.Bixby 將結合其去年於 INFORMS 2013 所發表與今年 INFORMS 2014 即將發表的最新研發成果與台灣學術及工業界做一次分享。同時·Dr. Bixby 也將非常樂於跟與會的台灣學者專家們做即席的問題討論及互動應答。

主辦單位:

- 台北科技大學工業工程與管理系
- 仁維軟體科技有限公司 (Firsttech Software)

報名及聯絡方式:

- 電子郵件報名: gurobi@firsttech-soft.com.tw 並說明單位及預計人數。
- 電話報名: 02-27712171 ext. 2376 (台北科大工管系製造資訊與商業智慧實驗室)
or 02-26828376 (仁維軟體)

美國國家工程學院院士 Dr. Robert Bixby 介紹



Dr. Robert Bixby has a BS in Industrial Engineering and Operations Research from the University of California, Berkeley, and a PhD in Operations Research from Cornell University. He has held academic positions at the University of Kentucky, Northwestern University, and Rice University. He is currently Noah Harding Professor Emeritus of Computational and Applied Mathematics at Rice, and Research Professor of Management in Rice's Jones School of Management.

Dr. Bixby has published over fifty journal articles, and is an acknowledged expert on the computational aspects of linear and integer programming. He has won several awards for his work in optimization, including the Beale-Orchard-Hays Prize of the Mathematical Programming Society, and the INFORMS Impact and Frederick W. Lanchester Prizes. In 1997 he was elected to the National Academy of Engineering for his contributions to the theory and practice of optimization.

Dr. Bixby has over twenty years of experience in the optimization software business. He co-founded CPLEX Optimization, Inc., in 1987. CPLEX was acquired by ILOG, Inc., in 1997, after which he served on the ILOG Board of Directors, manager of the ILOG CPLEX Development Team, President of the ILOG Technical Advisory Board, and General Manager of ILOG's Semiconductor Business Division.